

<b>FORM PTO-1449</b>  <b>LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT</b>  (Use several sheets if necessary)	ATTY. DOCKET NO. <b>10011537-1</b>	APPLICATION NO.	CONFIRMATION NO.
APPLICANT <b>Robert D. Gardner</b>			
FILING DATE <b>Herewith</b>		GROUP	

1017 U.S. PTO  
 10/033102  
 10/25/01

**REFERENCE DESIGNATION U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS
	1A	4,809,160	Feb. 28, 1989	Mahon et al.		
	1B	5,134,701	Jul. 28, 1992	Mueller et al.		
	1C	5,450,586	Sept. 12, 1995	Kuzara et al.		
	1D	5,465,335	Nov. 7, 1995	Anderson		
	1E	5,915,114	Jun. 22, 1999	McKee et al.		
	1F					
	1G					
	1H					
	1I					
	1J					
	1K					

**FOREIGN PATENT DOCUMENTS**

		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	TRANSLATION	
							YES	NO
	1L							
	1M							
	1N							
	1O							
	1P							

**OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)**

	1Q	Hewlett-Packard Product Manual, "Overview of IA-64 Architecture".
	1R	Hewlett-Packard Product Manual, "IA-64 Software Conventions and Runtime Architecture", Version 1.0 (August 31, 1999).
	1S	Intel Product Manual, "Intel IA-64 Architecture Software Developer's Manual", Volume 1: IA-64 Application Architecture, Revision 1:1 (July 2000).

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	2O							
	2P							

## OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

2Q	Intel Product Manuel, "Intel IA-64 Architecture Software Developer's Manual", Volume 2: IA-64 System Architecture, Revision 1.1 (July 2000).
2R	Intel Product Manuel, "Intel IA-64 Architecture Software Developer's Manual", Volume 3: IA-64 Instruction Set Reference, Revision 1.1 (July 2000).
2S	Intel Product Manuel, "Intel IA-64 Architecture Software Developer's Manual", Volume 4: Itanium Processor Programmer Guide, Revision 1.1 (July 2000).

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3Q	Intel Product Manual, "Intel IA-64 Architecture Software Developer's Manual", Itanium Processor Microarchitecture Reference for Software Optimization (August 2000).
3R	
3S	

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